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Introduction to Synthesis What is Application Specific Hardware - ASICS

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ASIC Design Flow.avi ASIC Design Flow Synopsys Design Compiler

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ASIC design flow

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Advanced ASIC Chip Synthesis: Using Synopsys® Design Compiler® and PrimeTime® is intended for anyone who is involved in the ASIC design methodology, starting from RTL synthesis to final tape-out. Target audiences for this book are practicing ASIC design engineers and graduate students undertaking advanced courses in ASIC chip ...

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